

	Search Text
1	"20020112091"
2	"20020112091" and adaptive same computing same element same ring
3	(fpga field near4 array) same ring and (fpga field near4 array) same chain\$4
4	((fpga field near4 array) same ring and (fpga field near4 array) same chain\$4) and acs
5	((fpga field near4 array) same ring and (fpga field near4 array) same chain\$4) and api
6	((fpga field near4 array) same ring and (fpga field near4 array) same chain\$4) and (cross near bar or switch)
7	((fpga field near4 array) same ring and (fpga field near4 array) same chain\$4) and (cross near bar or switch) and pci and api
8	((fpga field near4 array) same ring and (fpga field near4 array) same chain\$4) and (cross near bar or switch) and pci
9	((fpga field near4 array) same ring and (fpga field near4 array) same chain\$4) and pci and vme and accelarator
10	((fpga field near4 array) same ring and (fpga field near4 array) same chain\$4) and pci and vme and accelarat\$4
11	((fpga field near4 array) same ring and (fpga field near4 array) same chain\$4) and pci and vme
12	(fpga field near4 array) and adaptive adj comput\$4
13	((fpga field near4 array) and adaptive adj comput\$4) and ring and chain
14	(fpga field near4 array) and adaptive adj comput\$4 and pci
15	(fpga field near4 array) and adaptive adj comput\$4 and pci and @ad<20010103
16	(pci peripheral near5 component near5 intercon\$7) same bus same (conventional well adj known well\$6 standard off near3 shelf commercial\$4 near4 availa\$7)
17	(pci peripheral near5 component near5 intercon\$7) same bus same (well adj known well\$6 off near3 shelf commercial\$4 near4 availa\$7)
18	((pci peripheral near5 component near5 intercon\$7) same bus same (well adj known well\$6 off near3 shelf commercial\$4 near4 availa\$7)) and network same ring
19	(((pci peripheral near5 component near5 intercon\$7) same bus same (well adj known well\$6 off near3 shelf commercial\$4 near4 availa\$7)) and network same ring) and fpga
20	(((pci peripheral near5 component near5 intercon\$7) same bus same (well adj known well\$6 off near3 shelf commercial\$4 near4 availa\$7)) and network same ring) and fpga

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21	(pci peripheral near5 component near5 intercon\$7) same bus same (conventional well adj known well\$6 standard off near3 shelf commercial\$4 near4 availa\$7) and fpga
22	((pci peripheral near5 component near5 intercon\$7) same bus same (conventional well adj known well\$6 standard off near3 shelf commercial\$4 near4 availa\$7) and fpga) and network same ring
23	(pci peripheral near5 component near5 intercon\$7) same bus same (conventional well adj known well\$6 standard) and fpga and (off near3 shelf commercial\$4 near4 availa\$7)
24	((pci peripheral near5 component near5 intercon\$7) same bus same (conventional well adj known well\$6 standard) and fpga and (off near3 shelf commercial\$4 near4 availa\$7)) and network same ring
25	(pci peripheral near5 component near5 intercon\$7) same bus same (conventional well adj known well\$6 standard) same (off near3 shelf commercial\$4 near4 availa\$7)
26	((pci peripheral near5 component near5 intercon\$7) same bus same (conventional well adj known well\$6 standard) same (off near3 shelf commercial\$4 near4 availa\$7)) and network same ring
27	(acs adapt\$4 adj comput\$4) with accelerat\$5
28	((acs adapt\$4 adj comput\$4) with accelerat\$5) and network same ring
29	((((acs adapt\$4 adj comput\$4) with accelerat\$5) same (conventional well adj known well\$6 standard off near3 shelf commercial\$4 near4 availa\$7)) and network same ring
30	((acs adapt\$4 adj comput\$4) with accelerat\$5) same (conventional well adj known well\$6 standard off near3 shelf commercial\$4 near4 availa\$7)
31	((acs adapt\$4 adj comput\$4) with accelerat\$5) same (conventional well adj known well\$6 standard off near3 shelf commercial\$4 near4 availa\$7)
32	((acs adapt\$4 adj comput\$4) with accelerat\$5) same (conventional well adj known well\$6 standard off near3 shelf commercial\$4 near4 availa\$7) and fpga
33	((adapt\$4 near6 comput\$4 near6 system) with accelerat\$5) same (conventional well adj known well\$6 standard off near3 shelf commercial\$4 near4 availa\$7) same (board card node)
34	accelerat\$4 same (fpga)
35	accelerat\$4 same (fpga) same (pci vme)
36	accelerat\$4 same (fpga pci vme) same (well known conventional standard)
37	(accelerat\$4 same (fpga pci vme) same (well adj known conventional standard)) and network same ring
38	accelerat\$4 same (fpga pci vme) same (well adj known conventional standard)
39	accelerat\$4 same fpga same (pci vme) same (well adj known conventional standard)

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40	sram same fpga and network same ring
41	(sram same fpga and network same ring) and (pci vme)
42	sram same fpga same (conventional well adj known wellknown standard off near3 shelf commercial\$4 near4 availa\$7)
43	(sram same fpga same (conventional well adj known wellknown standard off near3 shelf commercial\$4 near4 availa\$7)) and network same ring
44	sram same fpga same (conventional well adj known wellknown standard off near3 shelf commercial\$4 near4 availa\$7) same xilinx
45	sram same fpga same (well adj known wellknown standard off near3 shelf commercial\$4 near4 availa\$7) same xilinx
46	sram same fpga same (well adj known wellknown standard off near3 shelf commercial\$4 near4 availa\$7) same xilinx and cross\$4 adj bar same (well adj known wellknown standard off near3 shelf commercial\$4 near4 availa\$7)
47	sram same fpga same (well adj known wellknown standard off near3 shelf commercial\$4 near4 availa\$7) same xilinx and cross\$4 adj bar
48	accelerator
49	accelerat\$5 same (PVI VME)
50	accelerat\$5 same (PVI VME) same network
51	accelerat\$5 same network same (well standard ordinary)
52	accelerat\$5 same network same (well adj known)
53	accelerator same network same (well adj known)